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| Search Notes  | Application/Control No. 09989777 | Applicant(s)/Patent Under Reexamination NEMECEK, CRAIG |
| | Examiner Jacob, Mary C | Art Unit 2123 |

| SEARCHED | | | |
|----------|----------|--------|----------|
| Class | Subclass | Date | Examiner |
| 703 | 23,26 | 1/6/10 | MCJ |
| 714 | 28 | 1/6/10 | MCJ |

| SEARCH NOTES | | |
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| Search Notes | Date | Examiner |
| Updated Inventor Name Search: EAST; Google Scholar | 1/6/10 | MCJ |
| Assignee Name Search: EAST, Google Scholar | 1/6/10 | MCJ |
| Updated East Search: see search history | 1/6/10 | MCJ |
| Updated Google Scholar Search: cypress semiconductor and sleep or stall, clocks; emulation sleep stall clocks "device under test"; emulation sleep clocks "device under test" lock step; emulation stall clocks "device under test" lockstep | 1/6/10 | MCJ |
| Updated IEEE Search: lockstep or lock step, sleep or stall, clock or clocks, device under test or DUT | 1/6/10 | MCJ |

| INTERFERENCE SEARCH | | | |
|---------------------|---|--------|----------|
| Class | Subclass | Date | Examiner |
| | Updated PGPUB/UPAD text search-see EAST search history printout | 1/6/10 | MCJ |

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